

FIG. 1

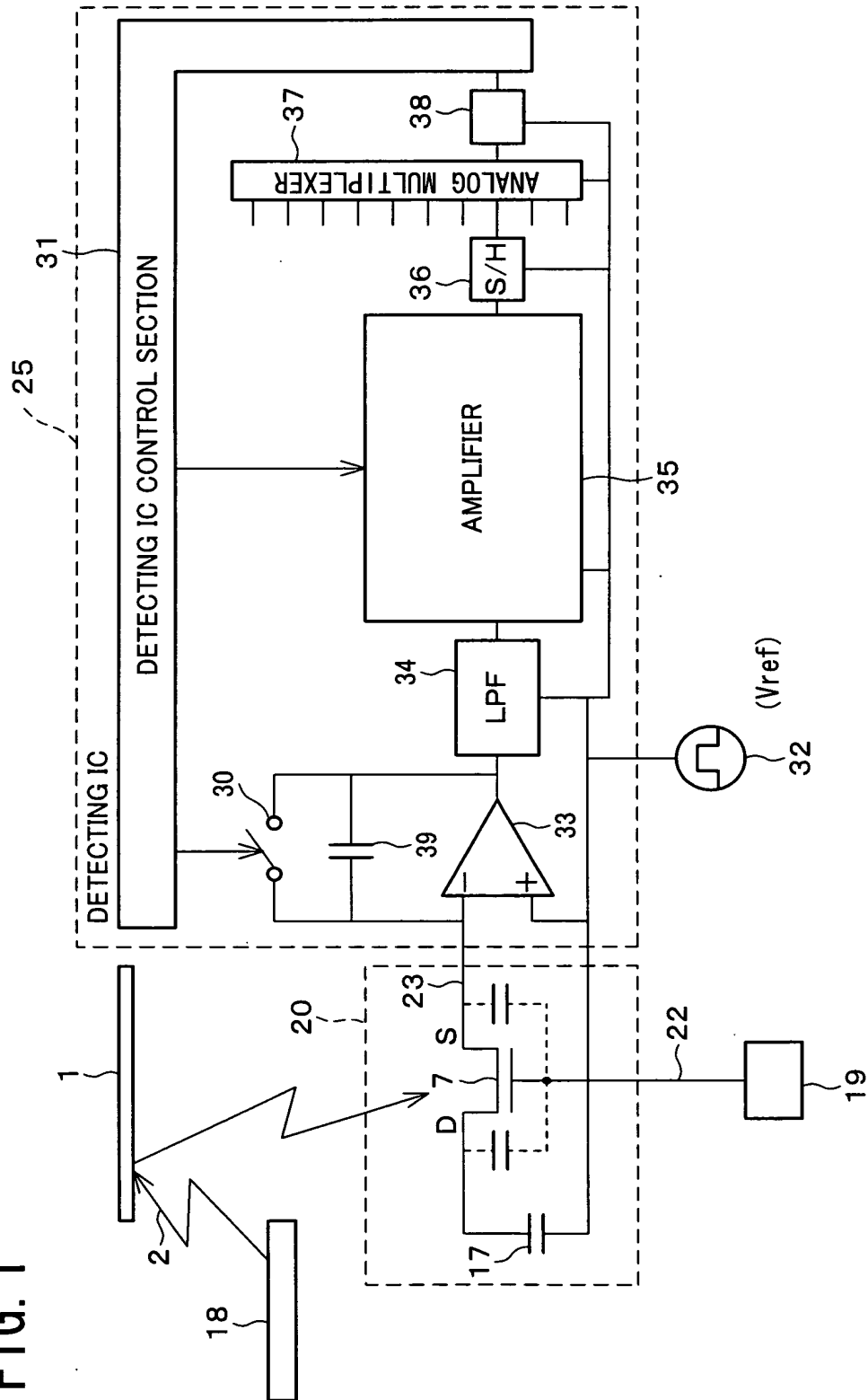


FIG. 2

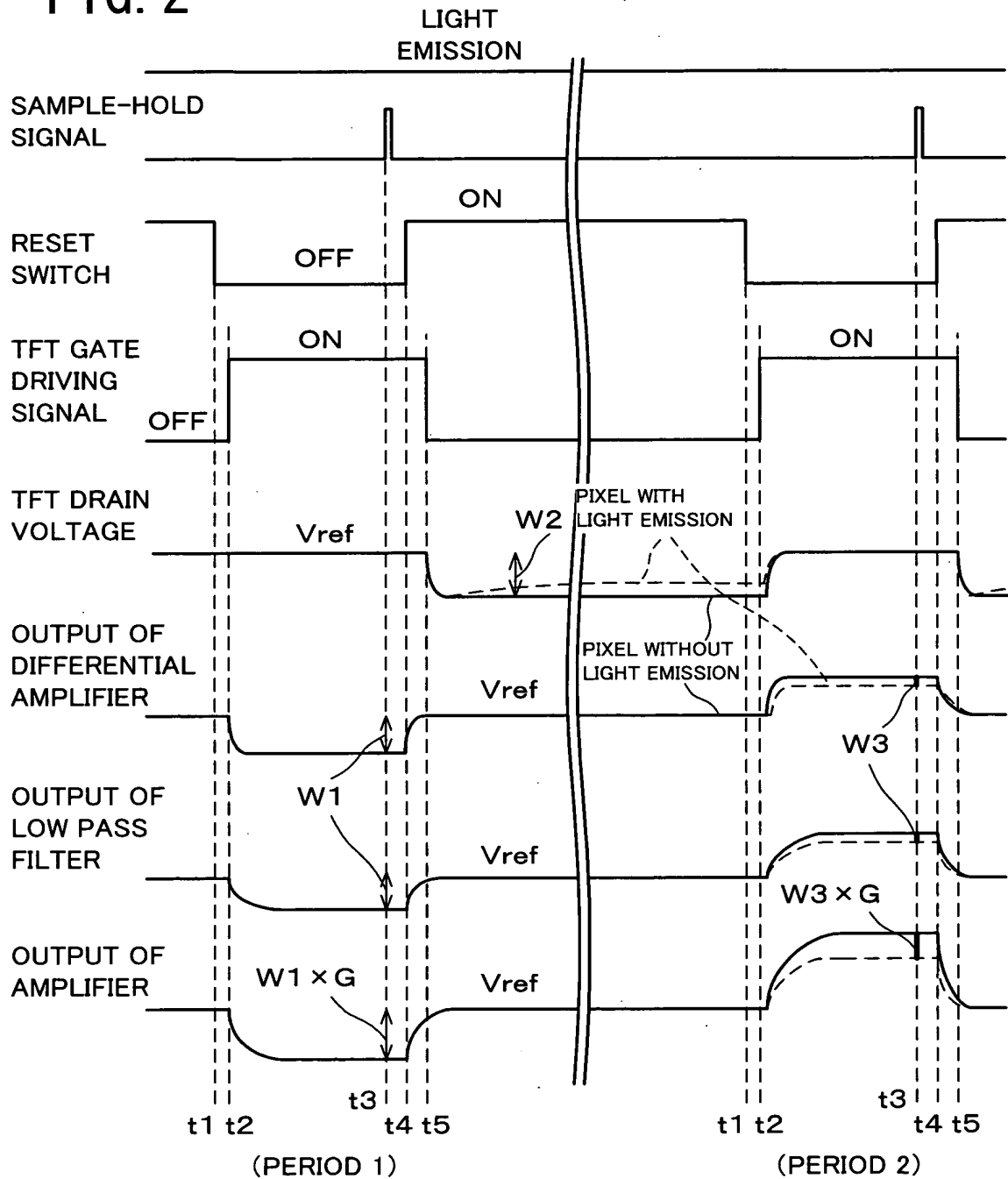
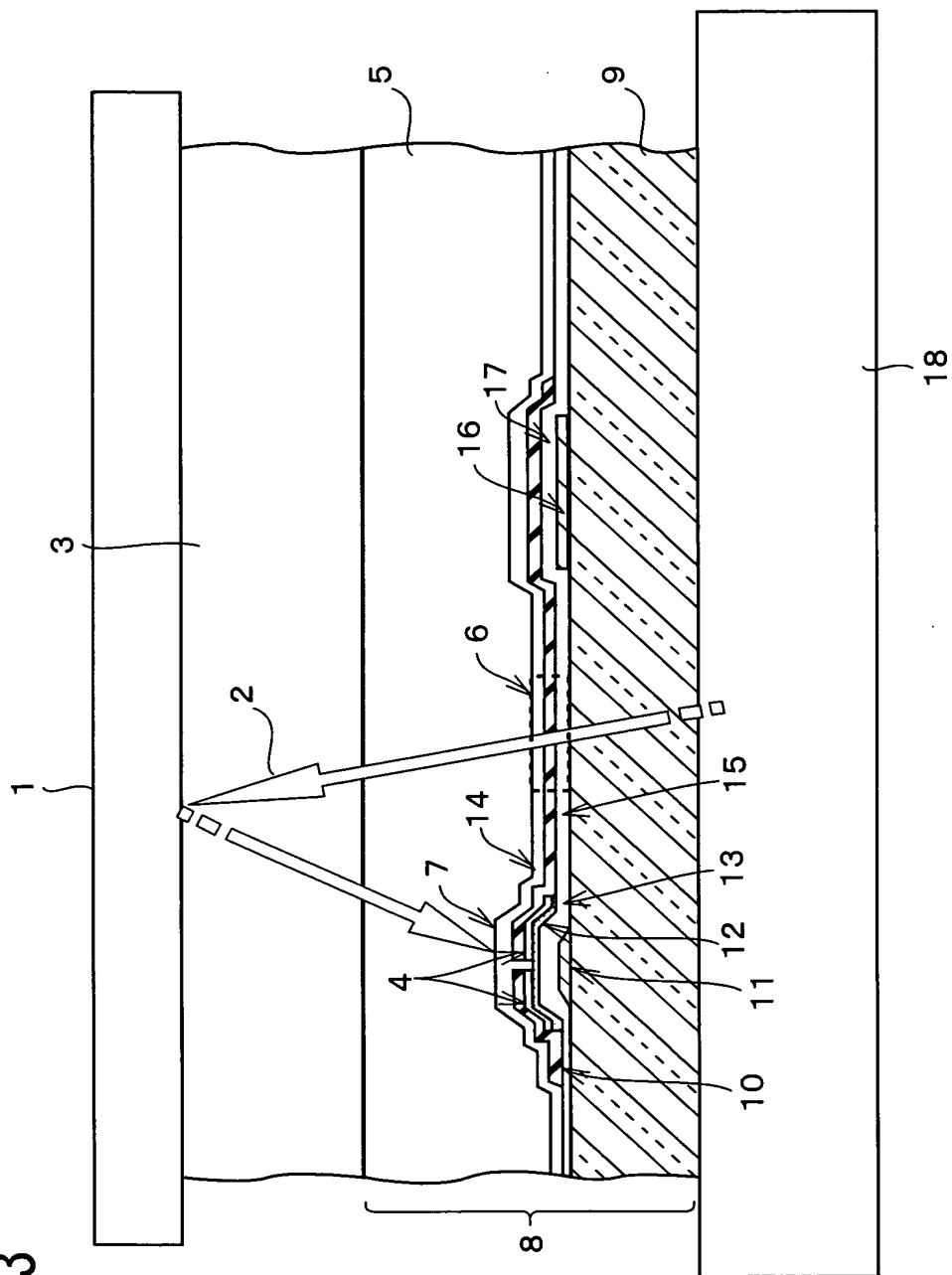
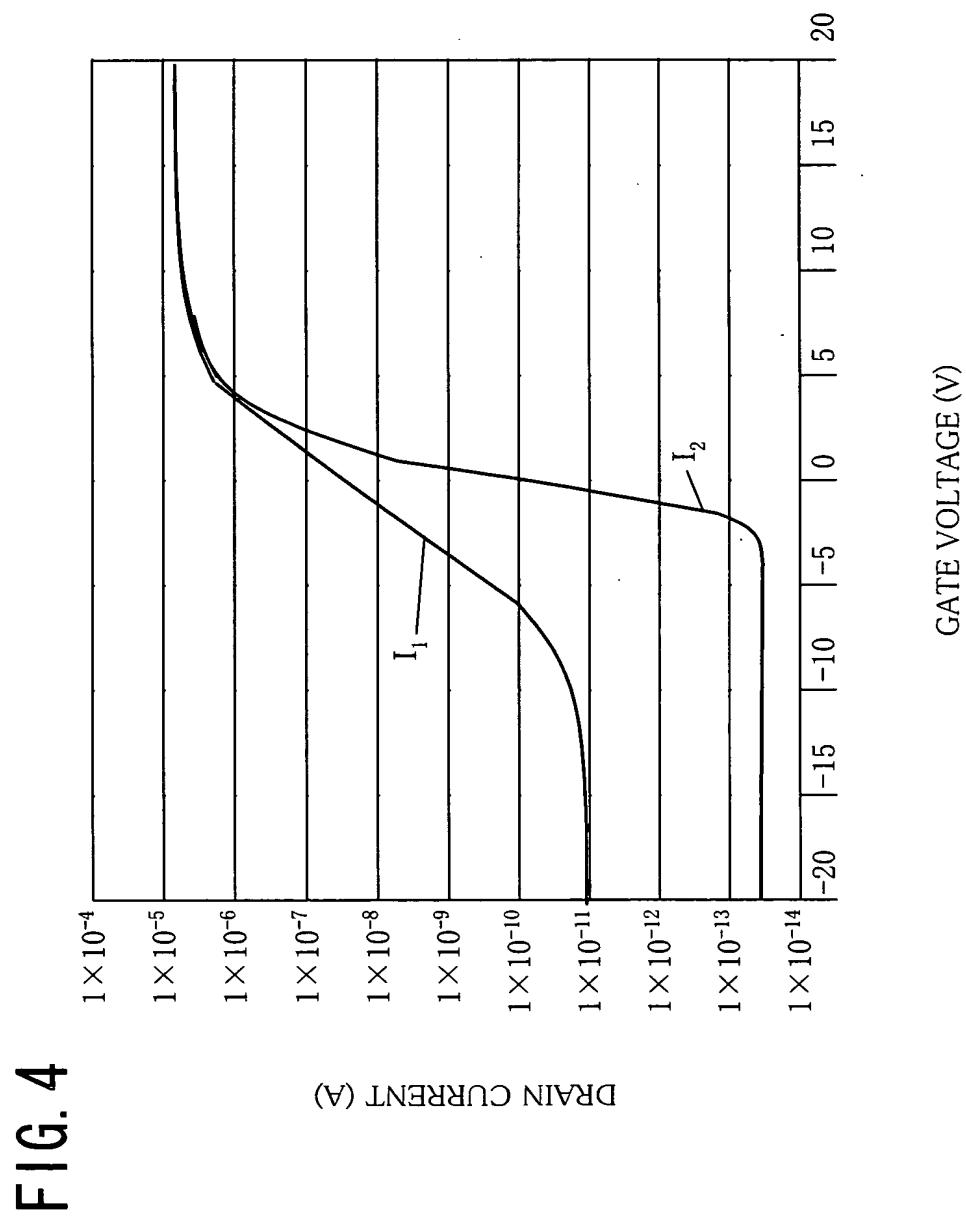


FIG. 3





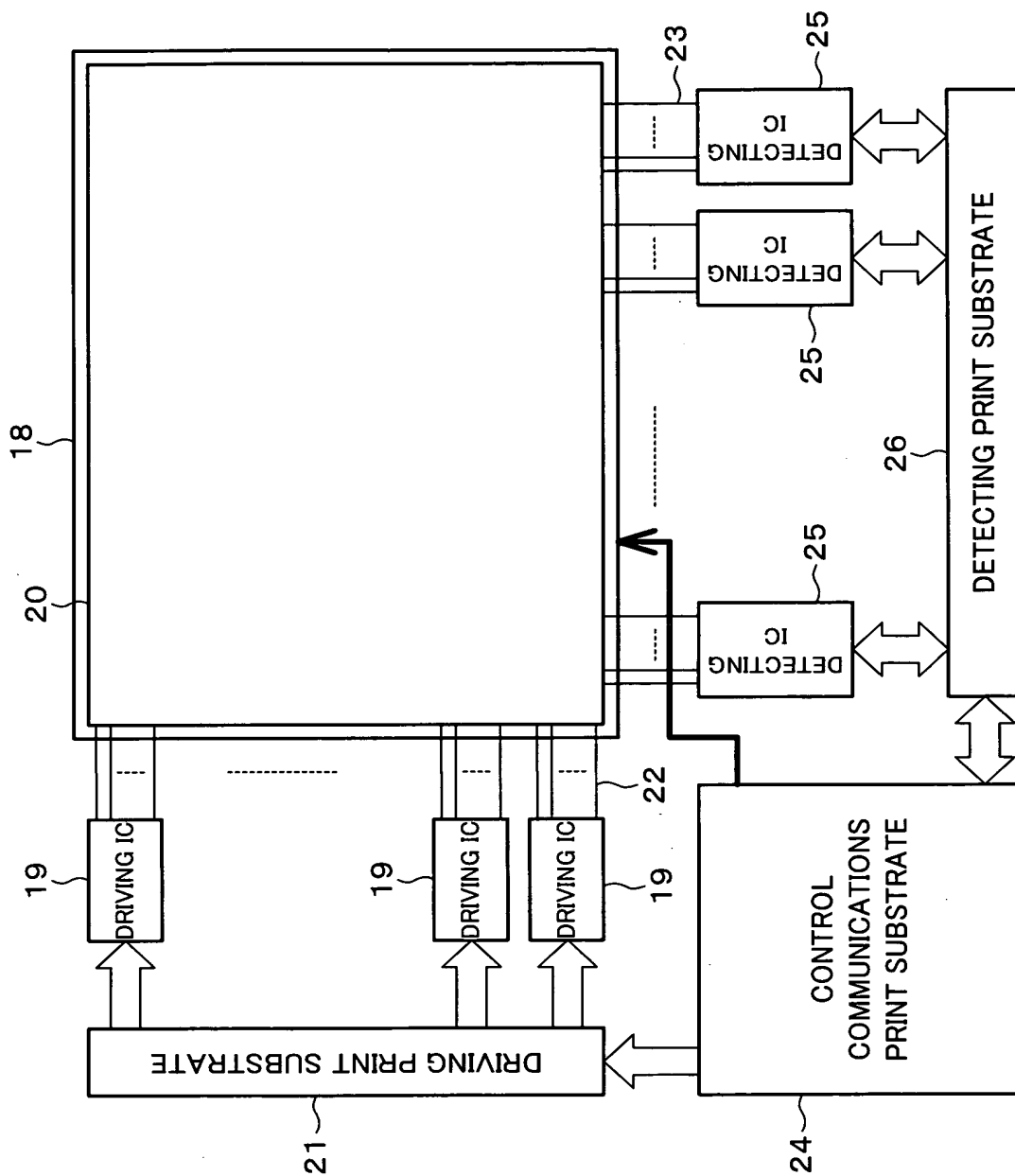


FIG. 5

FIG. 6

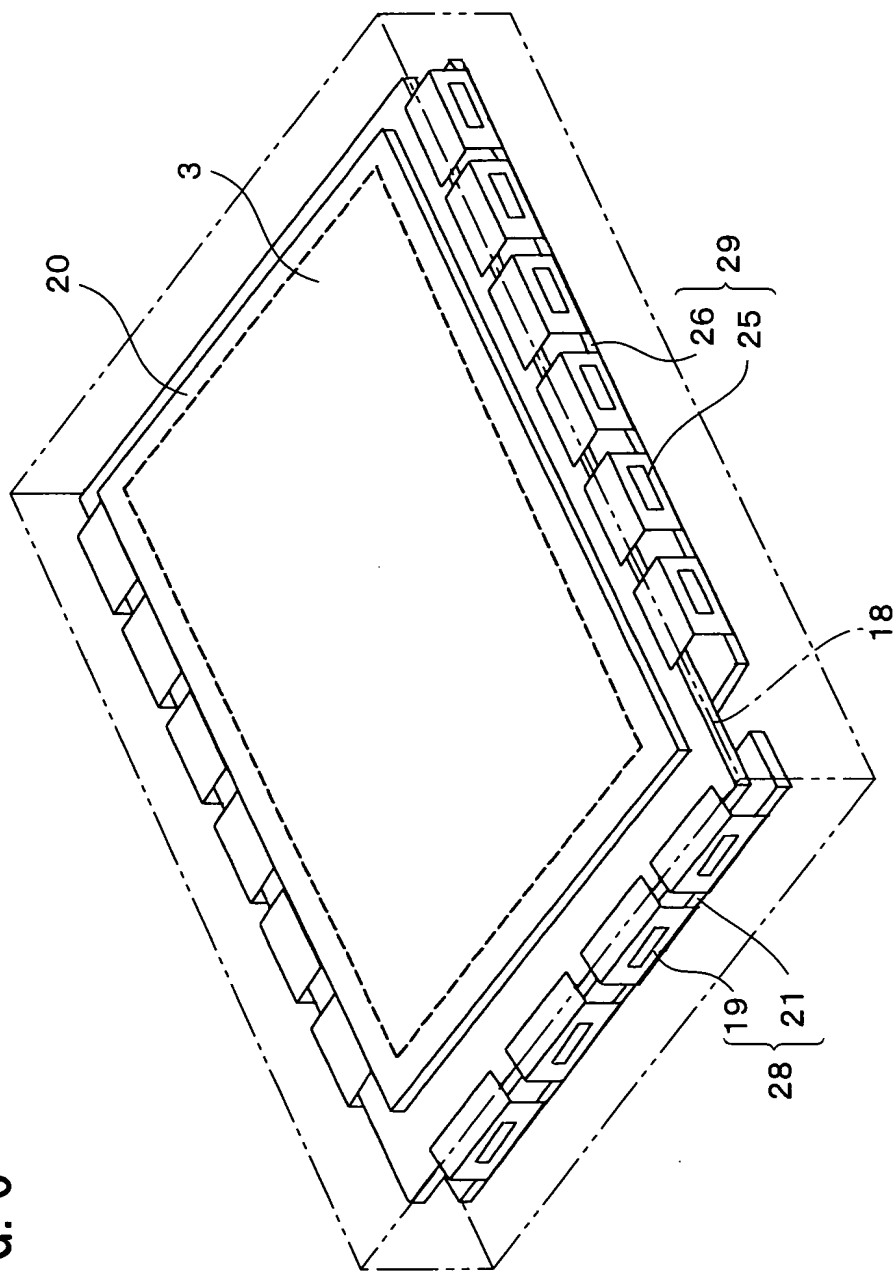


FIG. 7

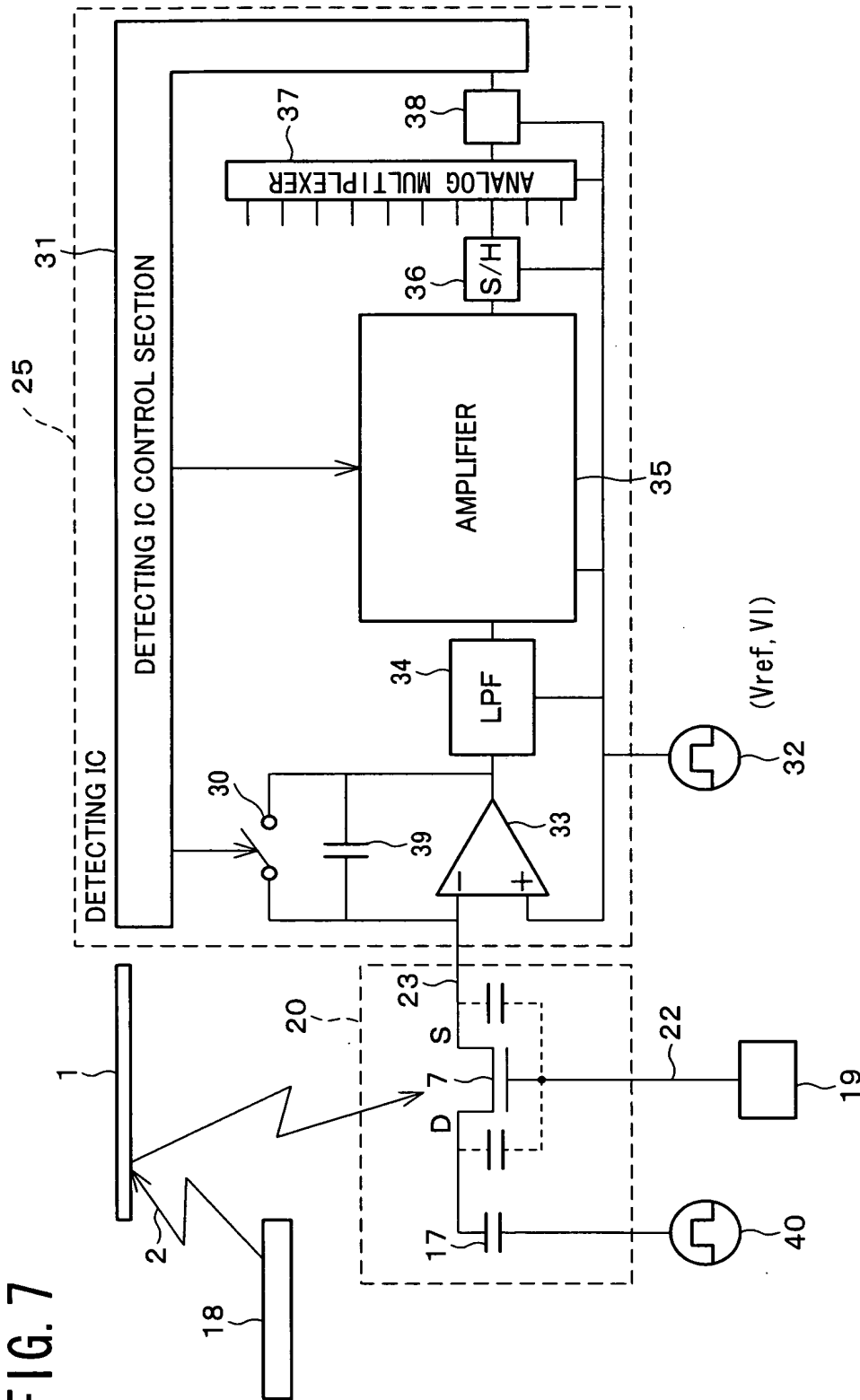


FIG. 8

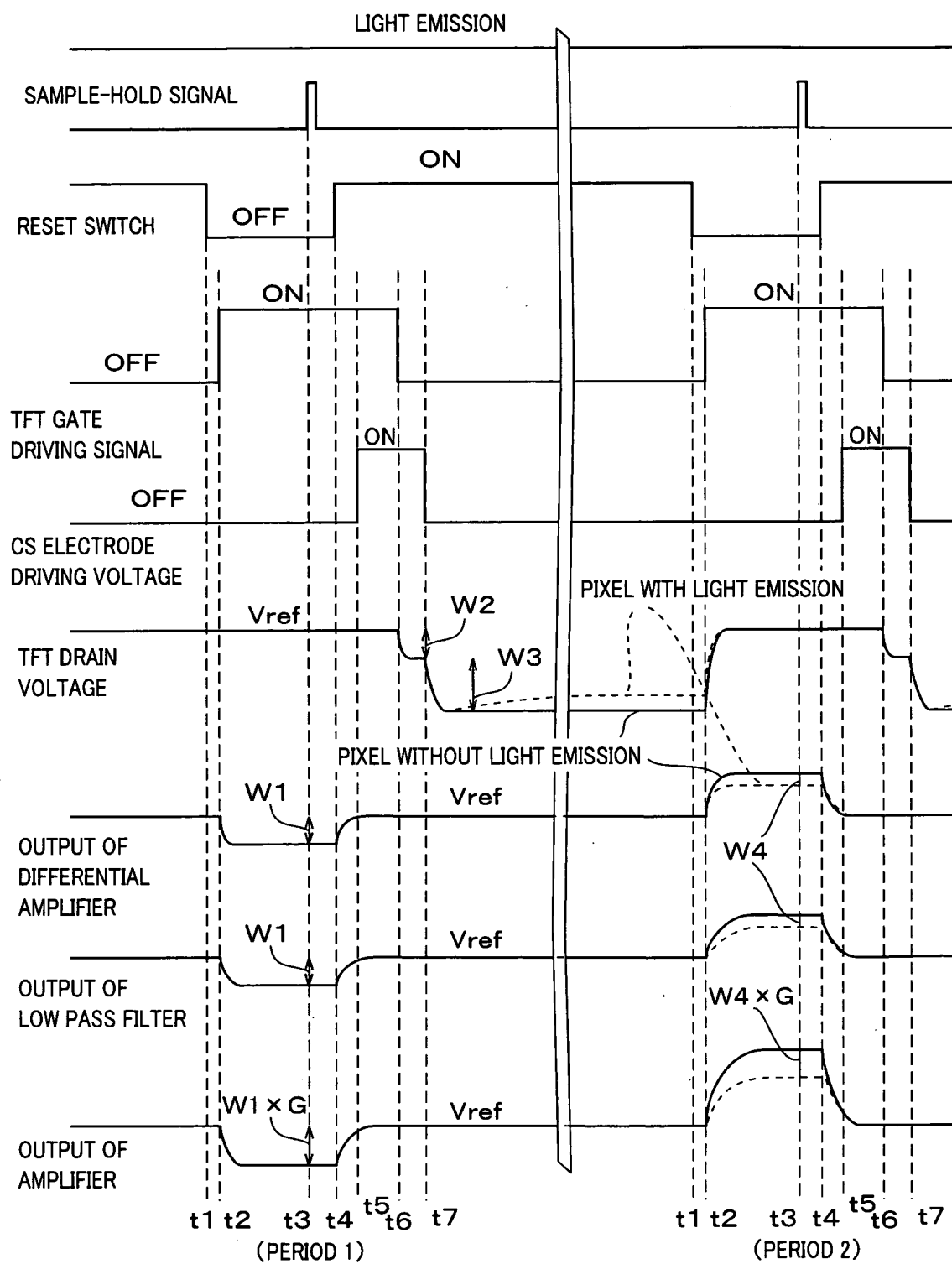


FIG. 9

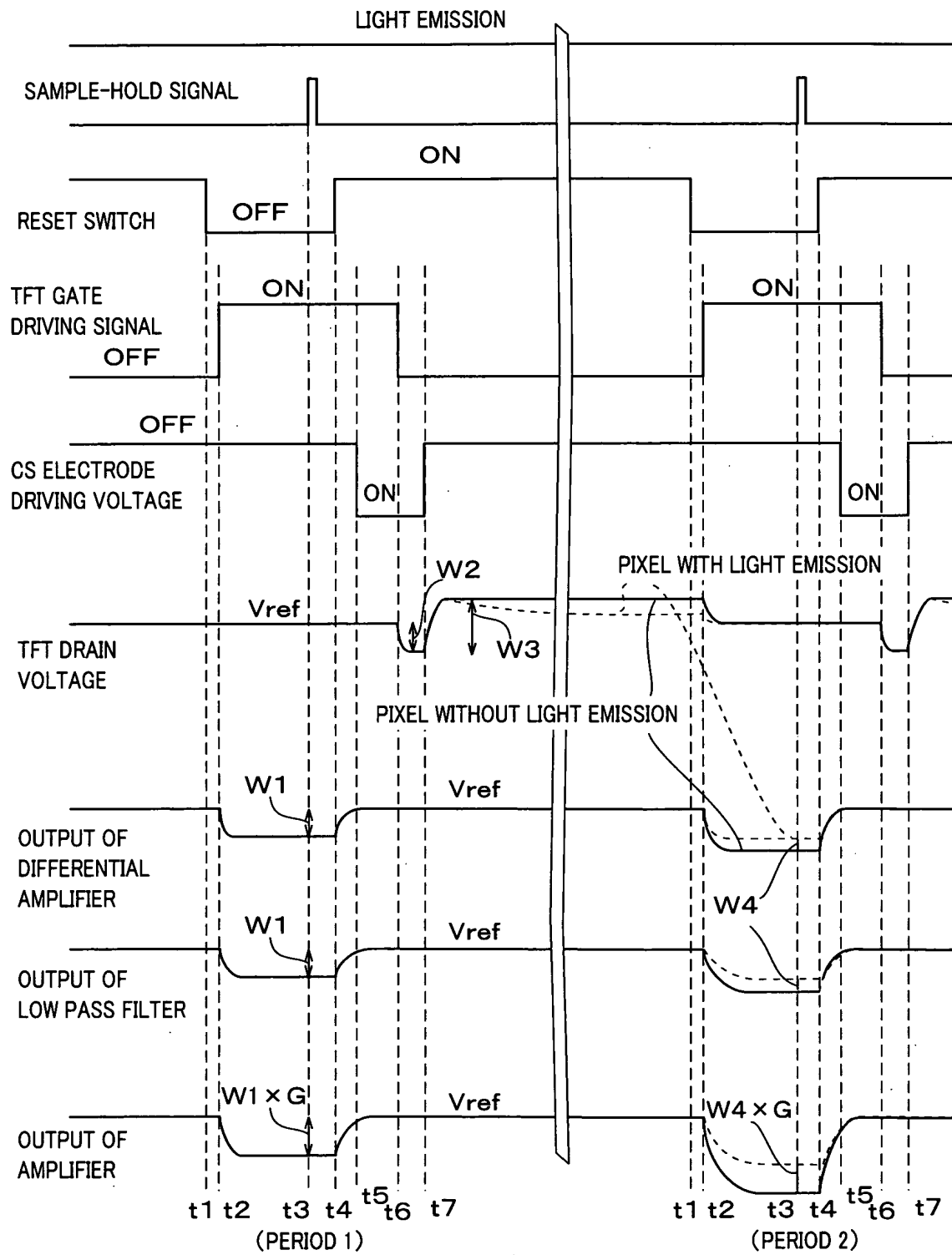
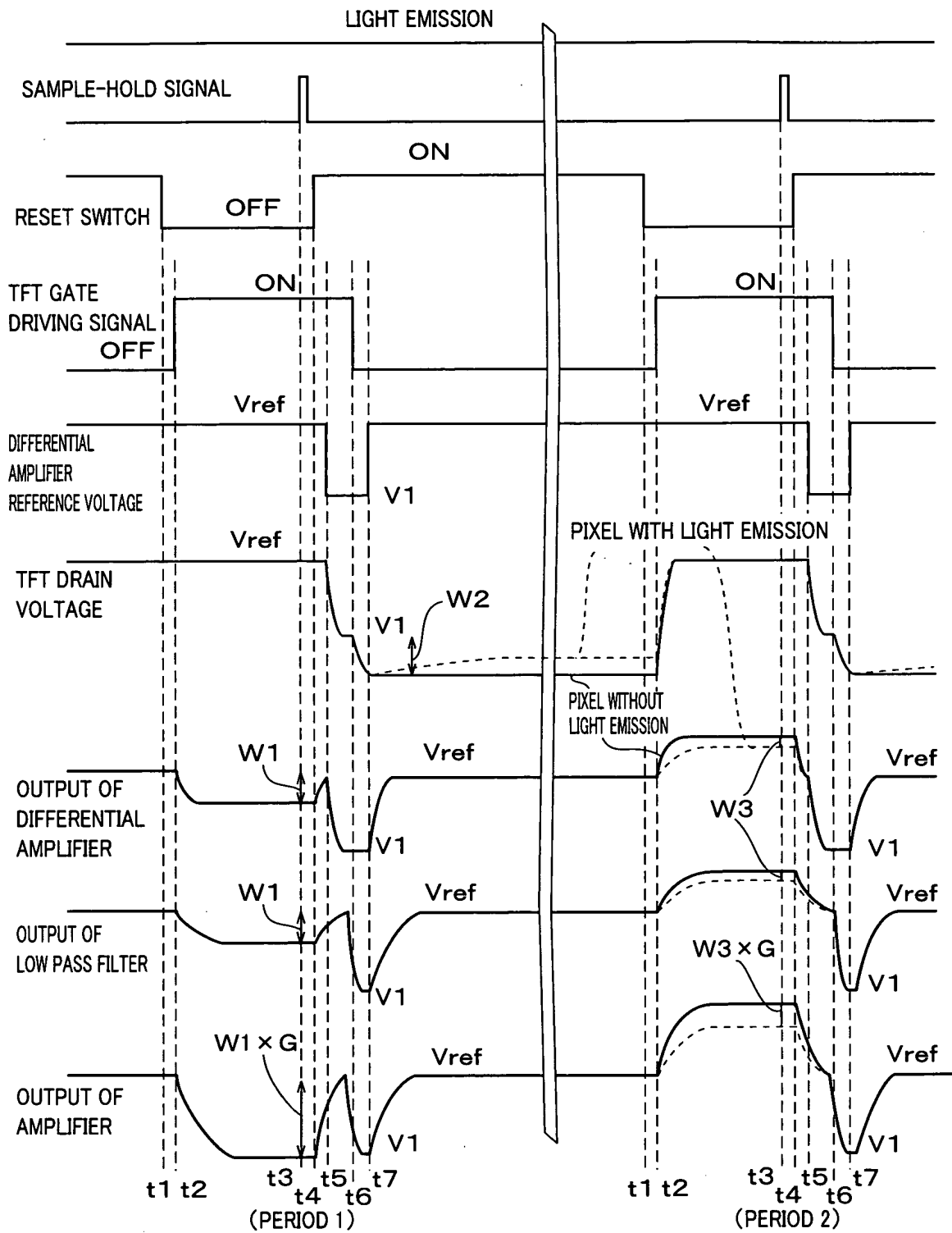


FIG. 10



The diagram illustrates the timing of various signals in a pixel circuit over two periods, (PERIOD 1) and (PERIOD 2). The signals shown are:

- LIGHT EMISSION:** A horizontal line indicating the light emission period, which occurs during the first half of each period.
- SAMPLE-HOLD SIGNAL:** A signal that is high during the light emission period and low otherwise.
- RESET SWITCH:** A signal that is high (ON) during the light emission period and low (OFF) otherwise.
- TFT GATE DRIVING SIGNAL:** A signal that is high (ON) during the light emission period and low (OFF) otherwise.
- DIFFERENTIAL AMPLIFIER REFERENCE VOLTAGE:** A signal that is high (Vref) during the light emission period and low (V1) otherwise.
- TFT DRAIN VOLTAGE:** A signal that is high (Vref) during the light emission period and low (V1) otherwise. It shows a peak during the light emission period.
- OUTPUT OF DIFFERENTIAL AMPLIFIER:** A signal that is high (V1) during the light emission period and low (Vref) otherwise. It shows a peak during the light emission period.
- OUTPUT OF LOW PASS FILTER:** A signal that is high (V1) during the light emission period and low (Vref) otherwise. It shows a peak during the light emission period.
- OUTPUT OF AMPLIFIER:** A signal that is high (V1) during the light emission period and low (Vref) otherwise. It shows a peak during the light emission period.

The diagram is divided into two main sections: **PIXEL WITH LIGHT EMISSION** and **PIXEL WITHOUT LIGHT EMISSION**. The signals are plotted against time, with specific time points labeled: t1, t2, t3, t4, t5, t6, and t7. The signals are labeled with their respective values: V1, Vref, W1, W2, W3, and W1 x G. The signals are also labeled with their respective functions: DIFFERENTIAL AMPLIFIER REFERENCE VOLTAGE, TFT DRAIN VOLTAGE, OUTPUT OF DIFFERENTIAL AMPLIFIER, OUTPUT OF LOW PASS FILTER, and OUTPUT OF AMPLIFIER.

FIG. 12

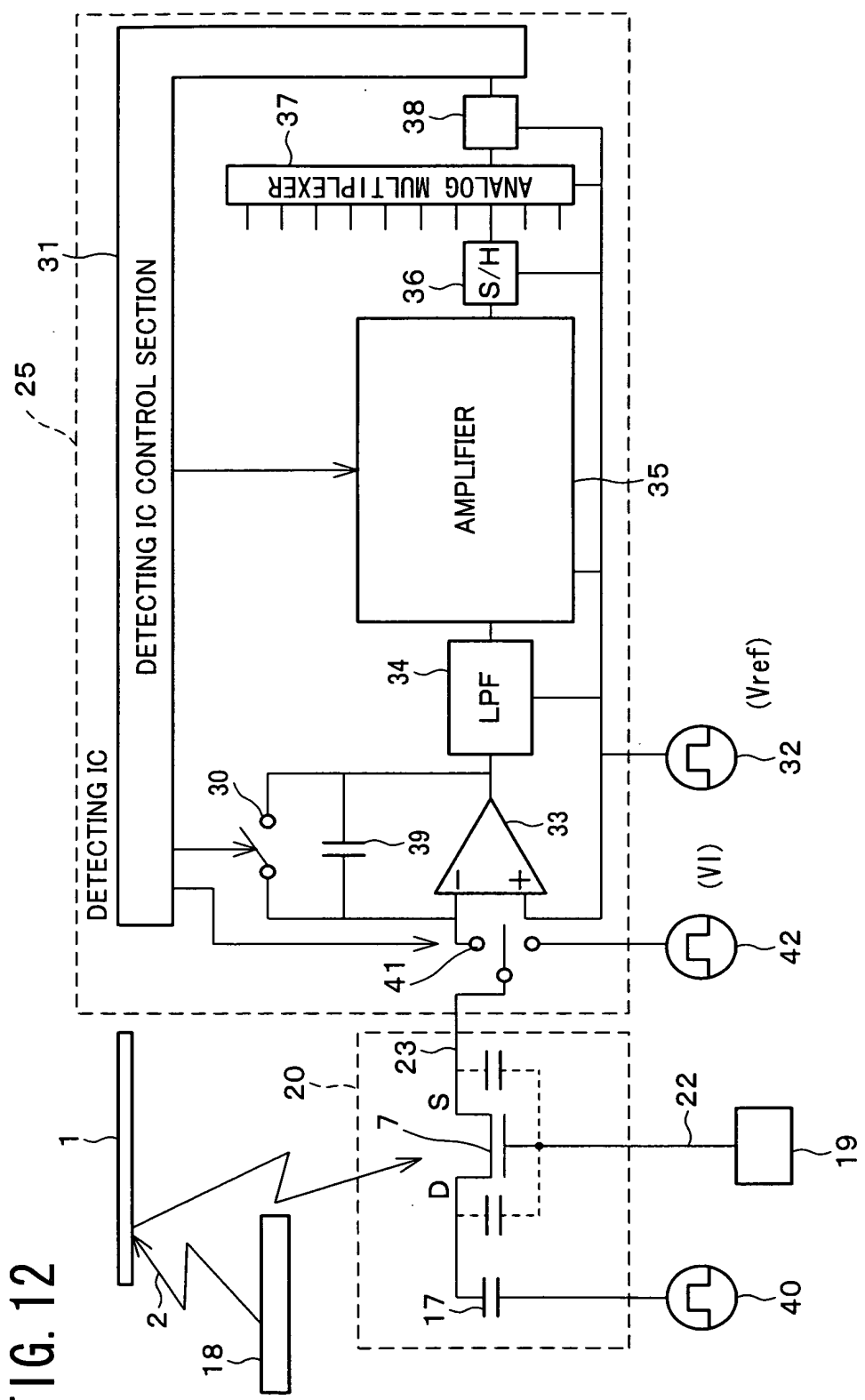


FIG. 13

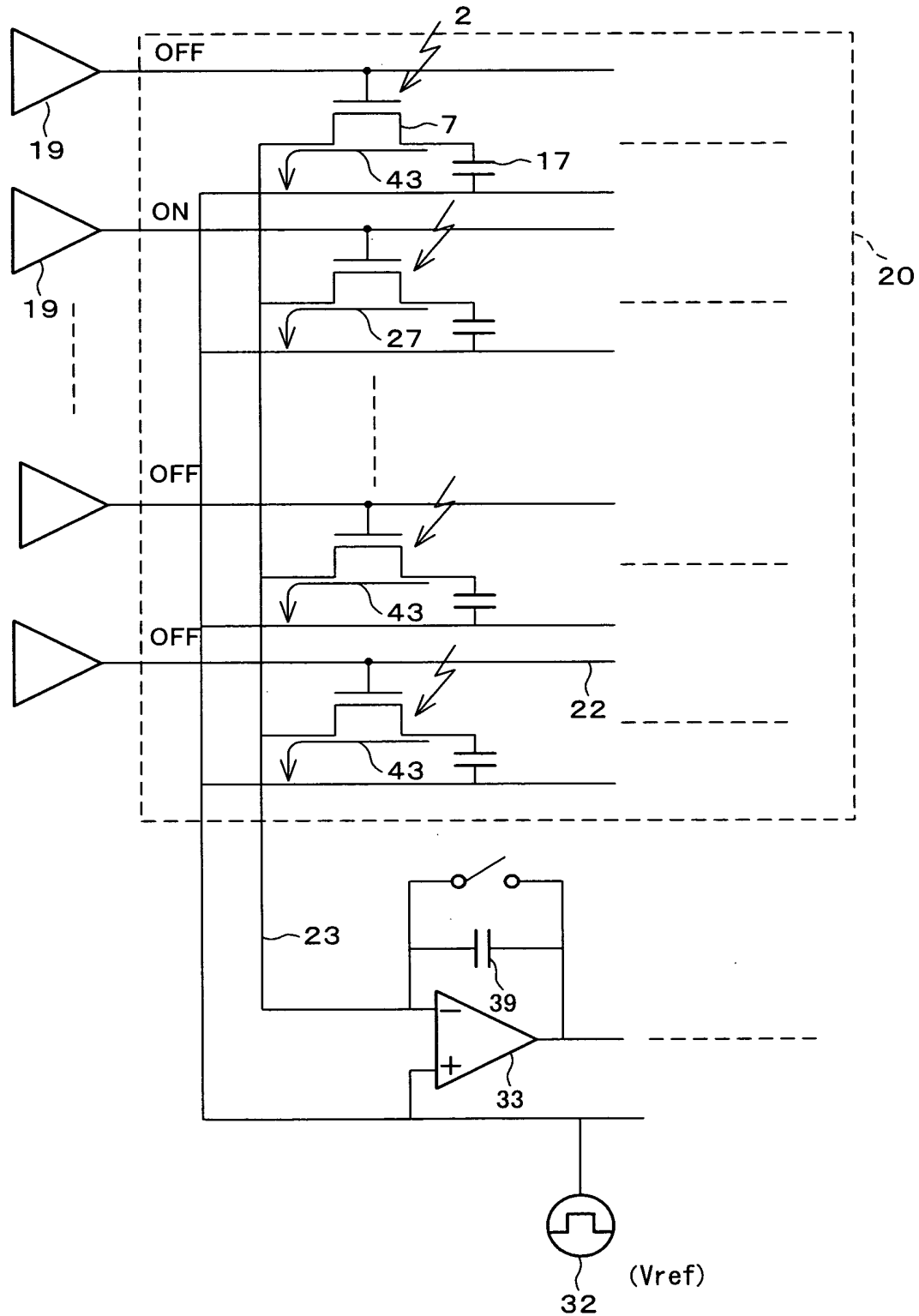


FIG. 14

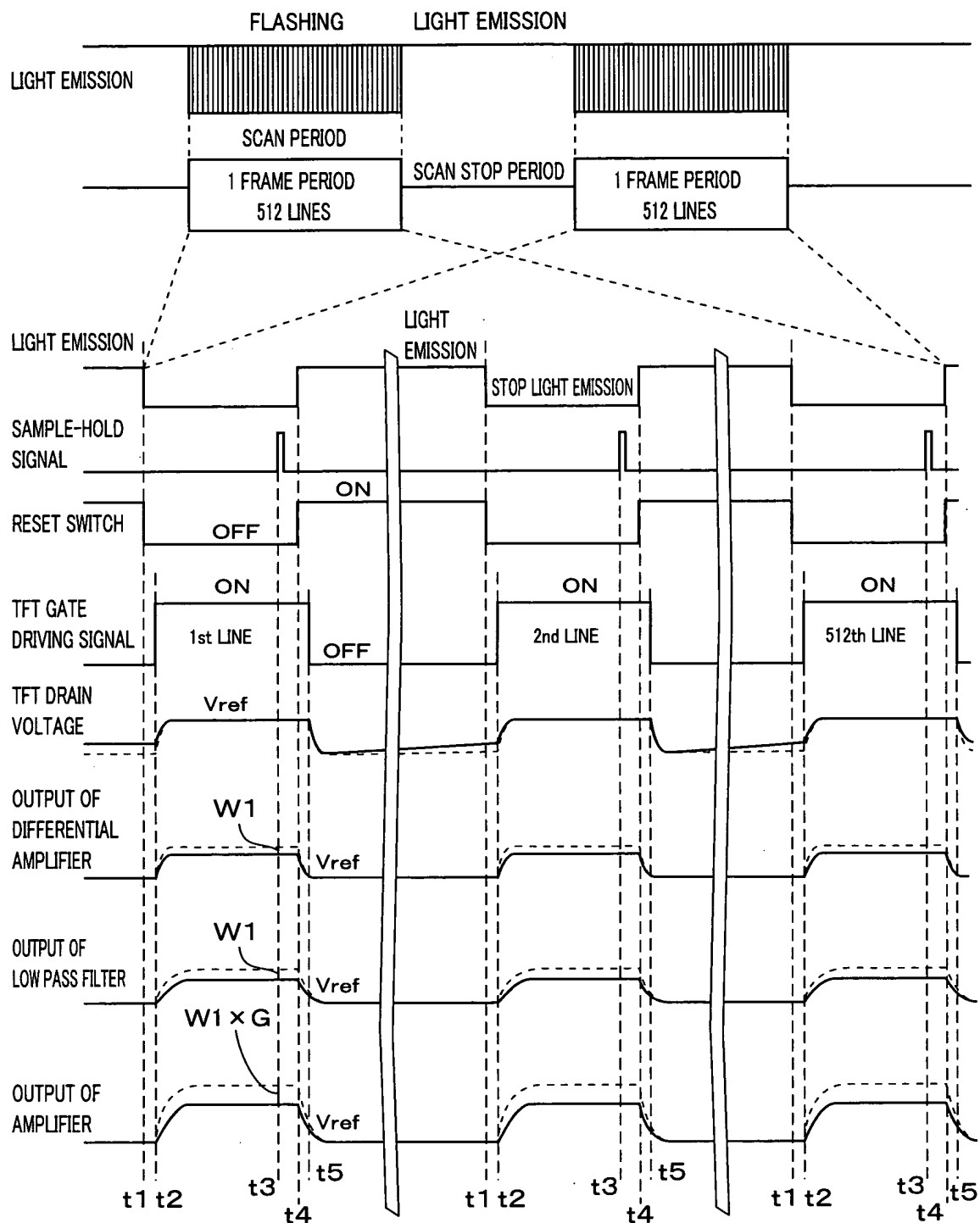


FIG. 15

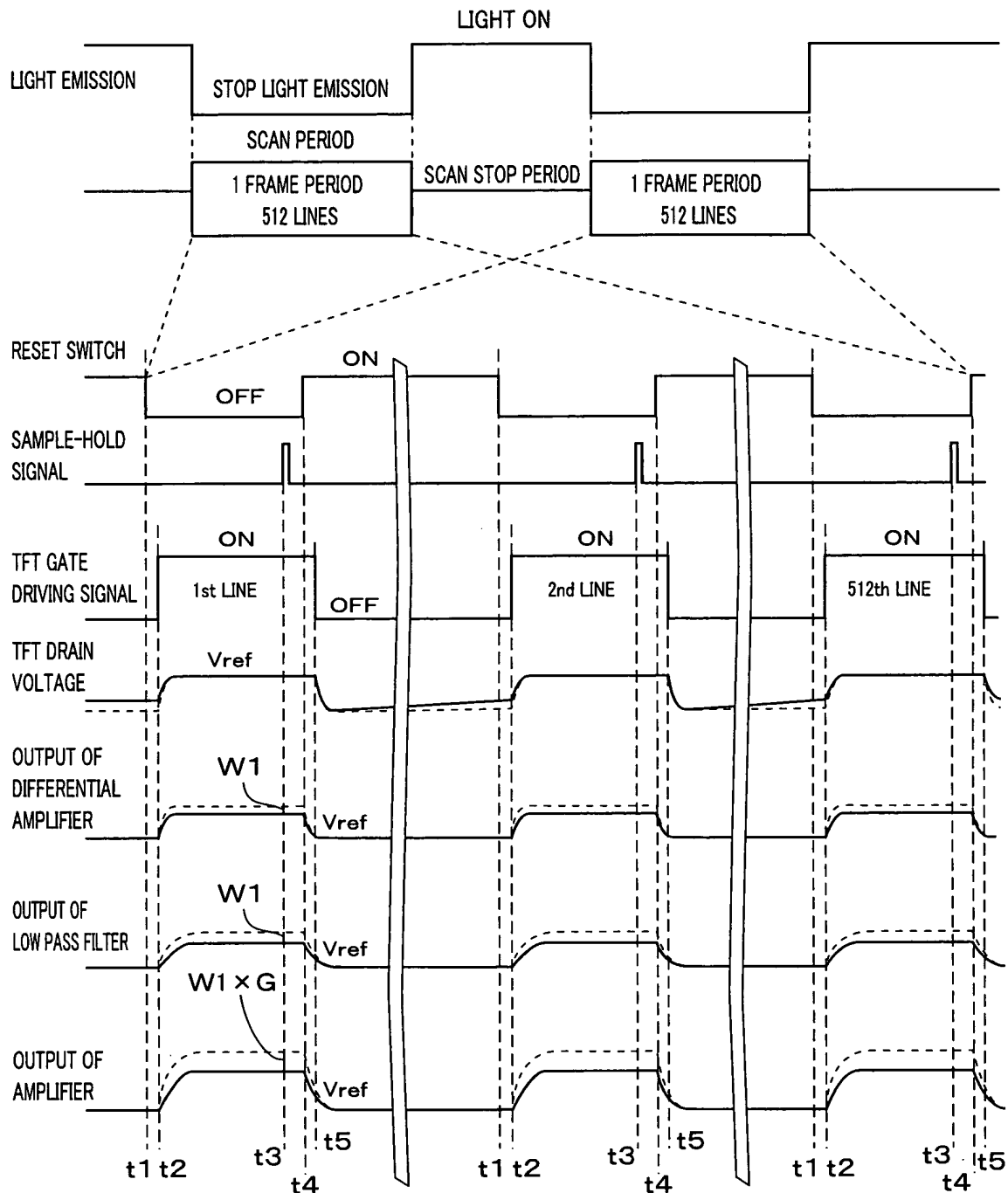


FIG. 16

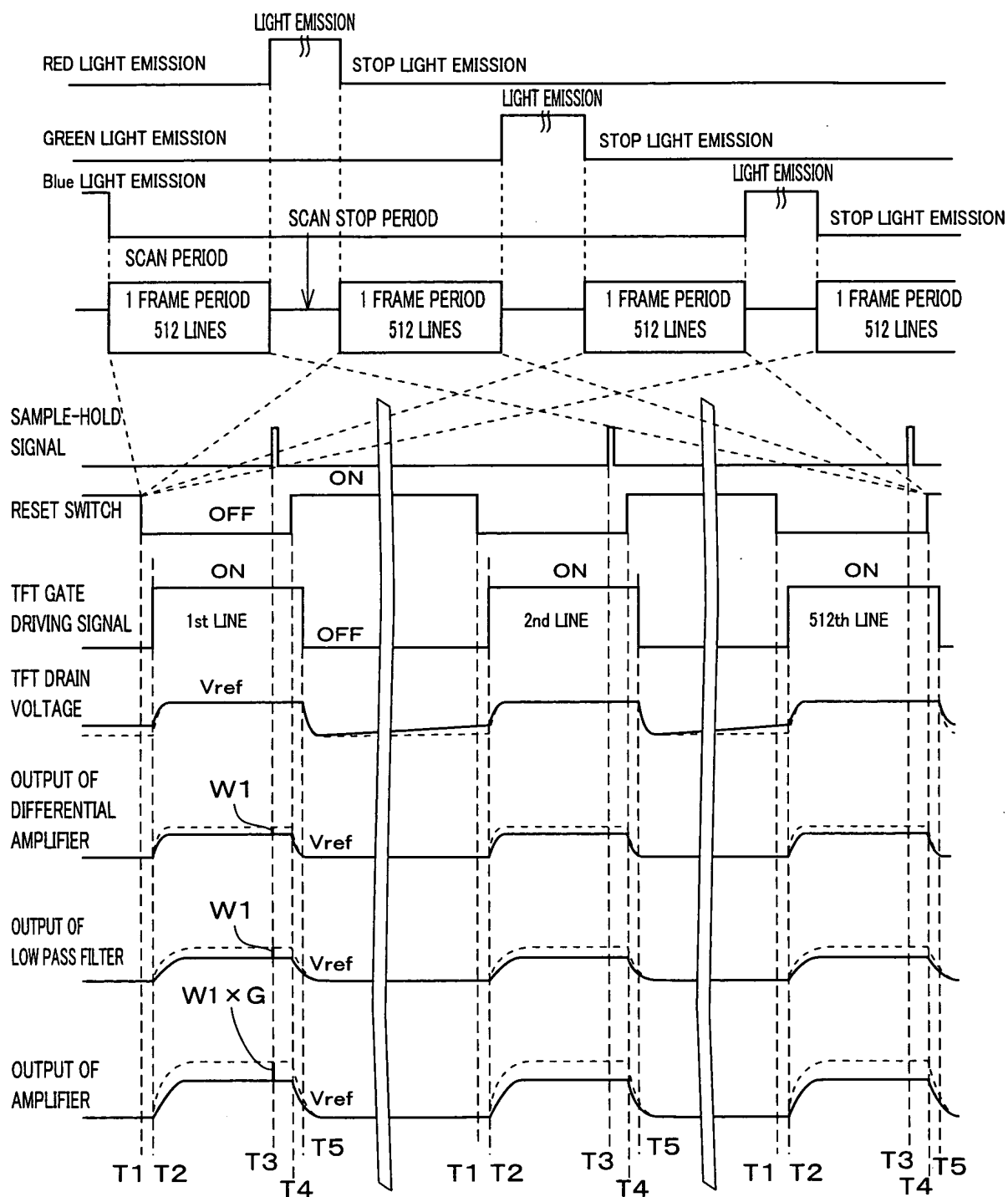


FIG. 17

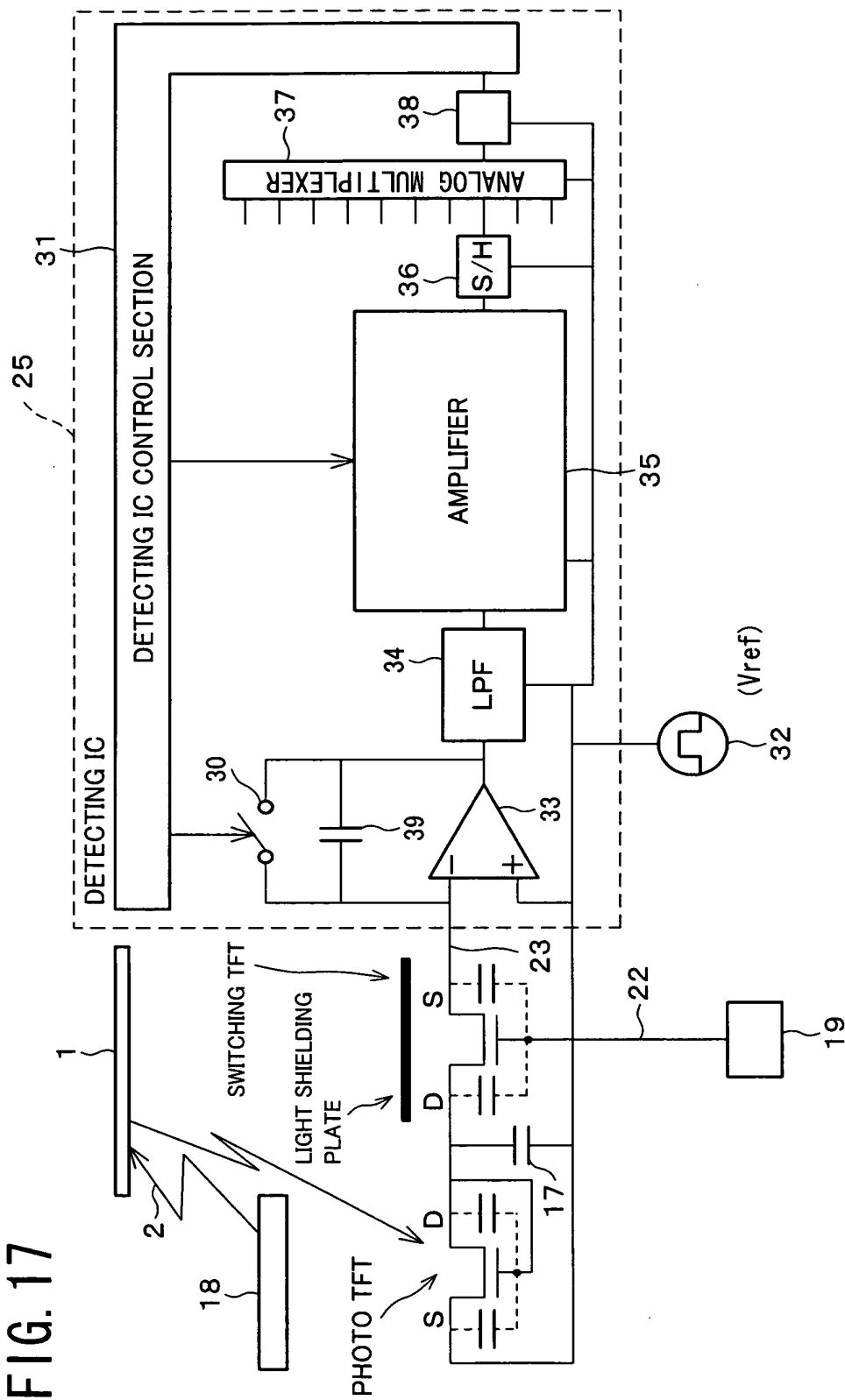


FIG. 18

